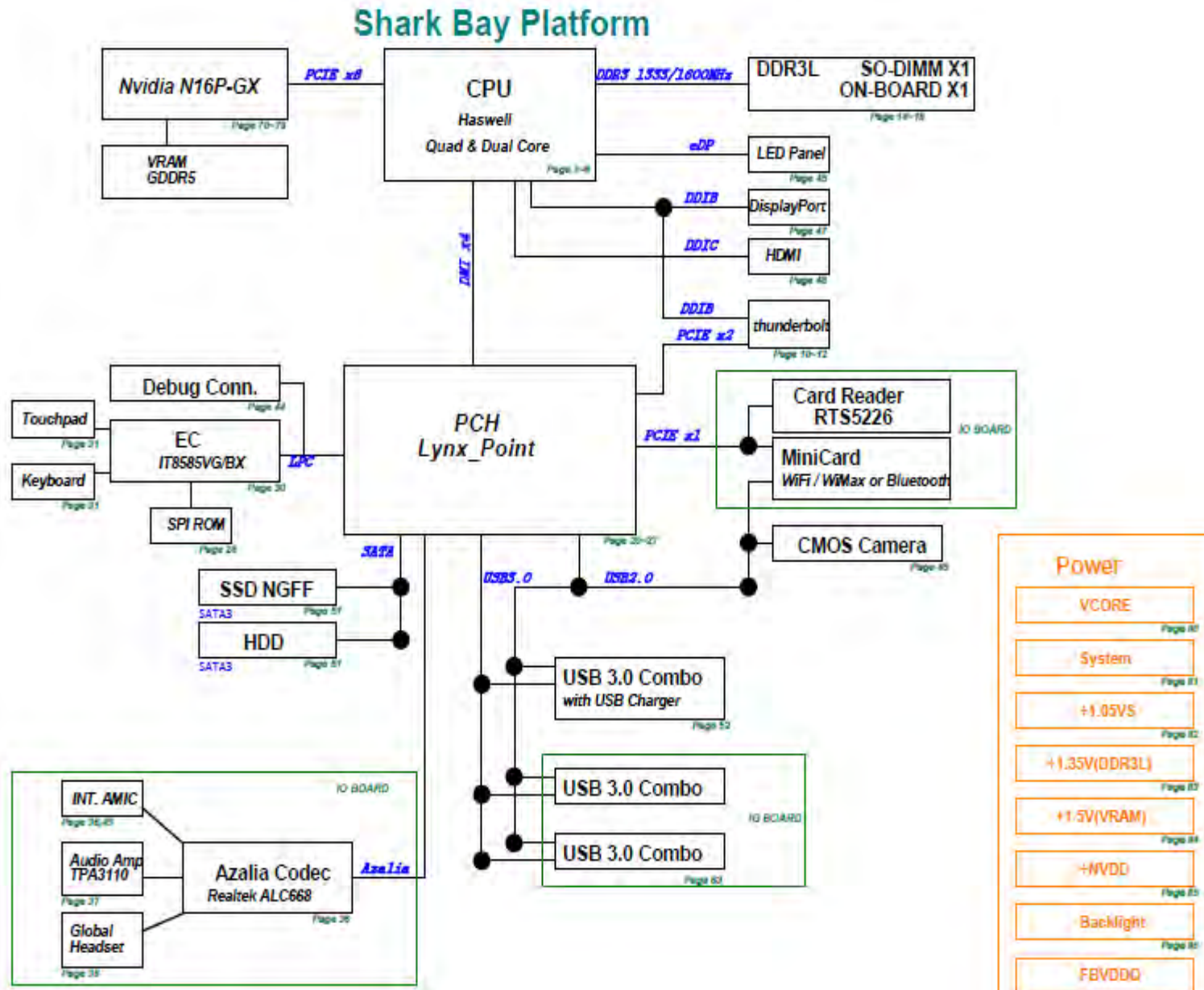
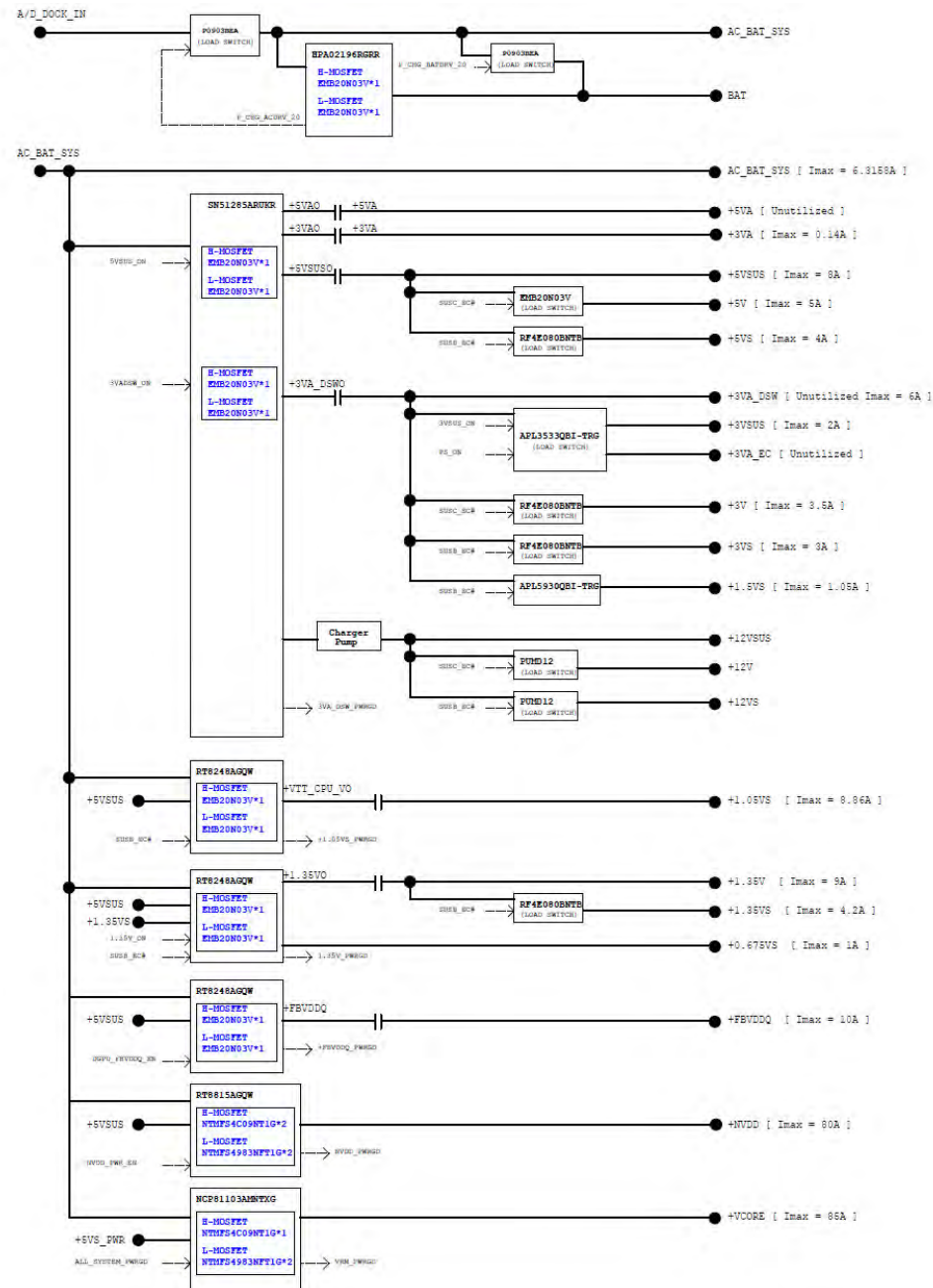


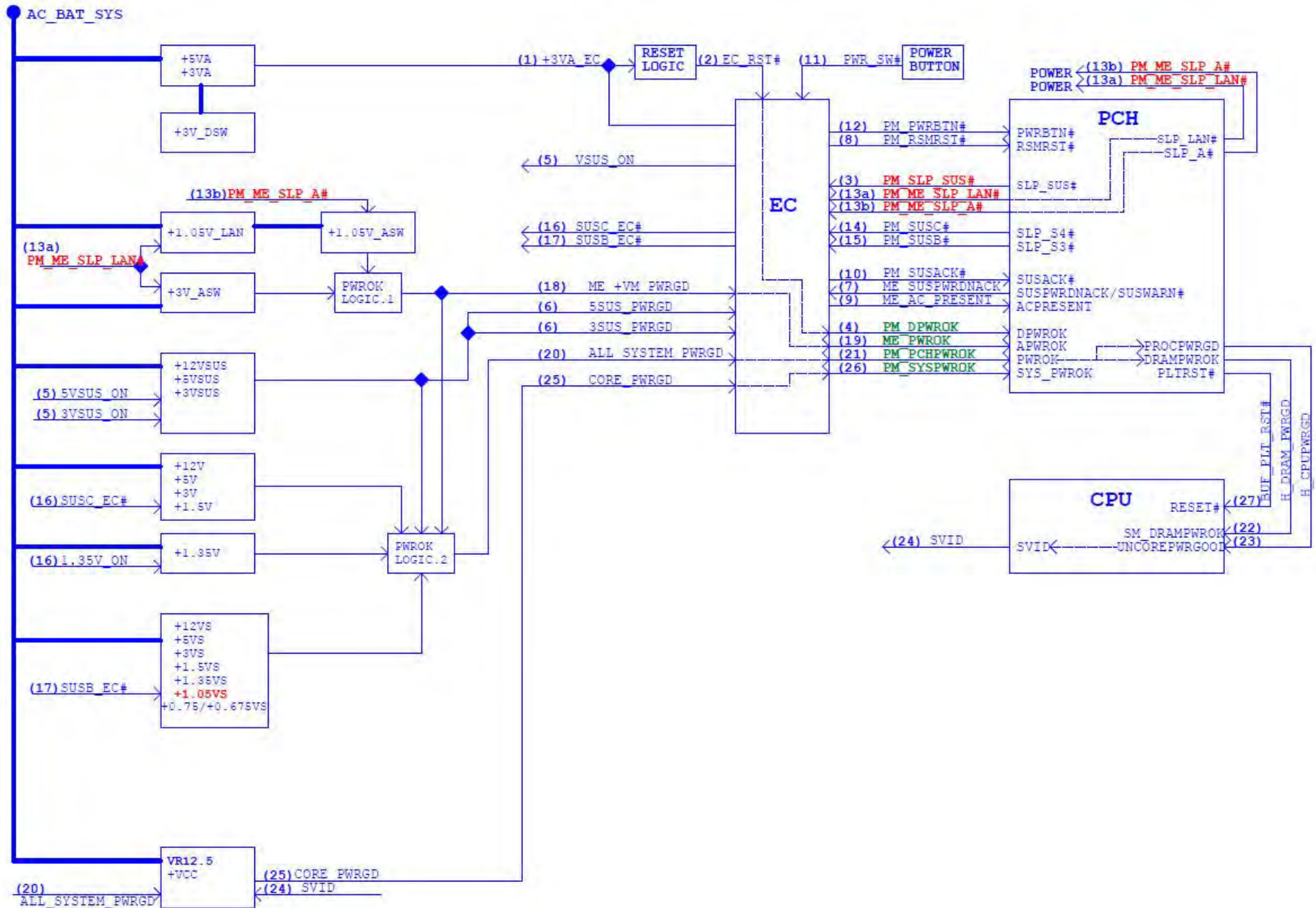
## BLOCK DIAGRAM



# POWER FLOW



# POWER ON SEQUENCE

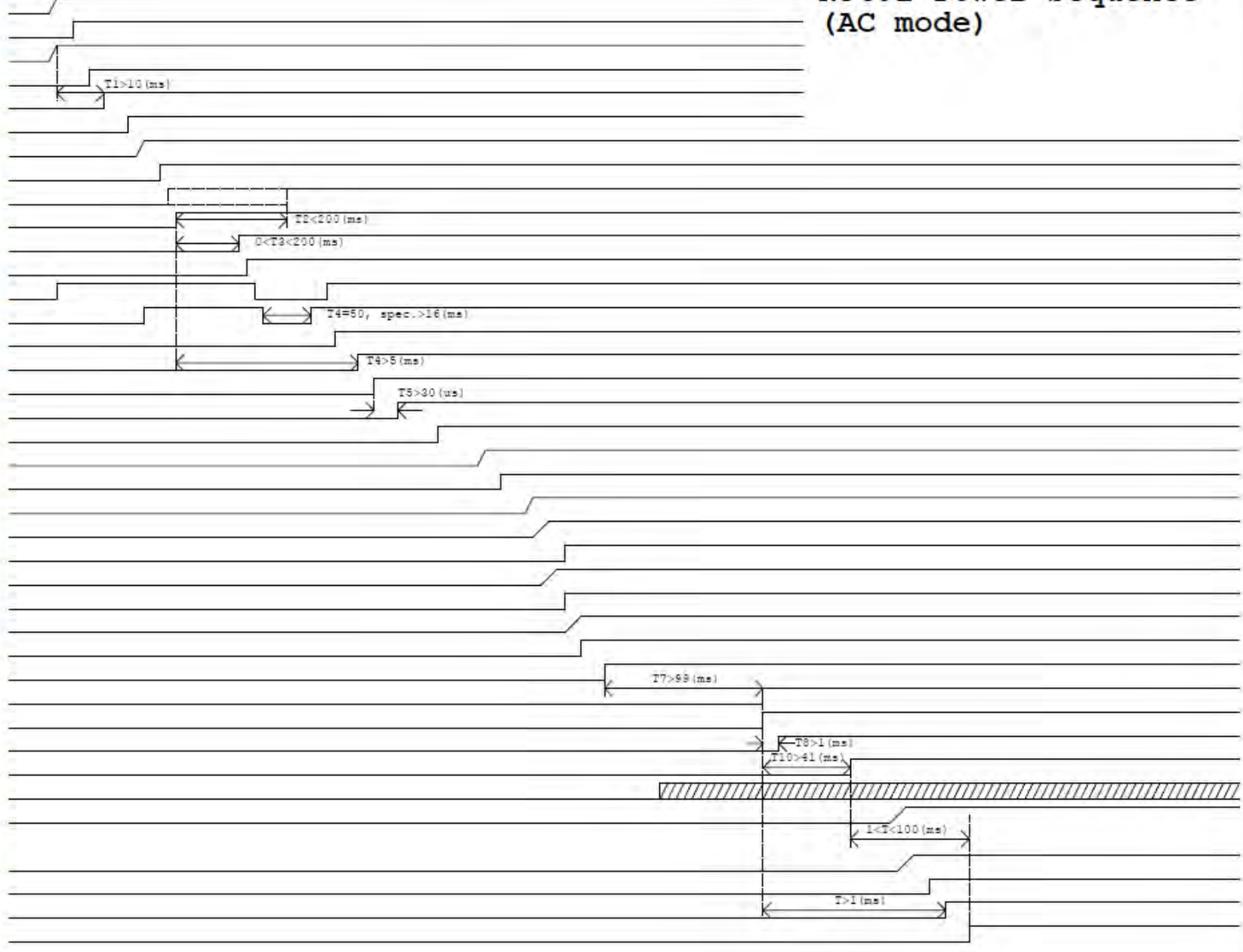


# AC\_IN POWER ON SEQUENCE

## AC-IN Mode

- 1 +3VA/+5VA/+3VA\_EC
- 2 EC\_RST#  
VccDSW
- 3 PM\_SLP\_SUS#
- 4 PM\_DPWR0K
- 5 VSUS\_ON  
+3VSUS/+5VSUS  
SUS\_PWRGD
- 7 ME\_SusPwrDnAck
- 8 PM\_RSMRST#
- 9 ME\_AC\_PRESENT
- 10 PM\_SUSACK#
- 11 PWR\_SW#
- 12 PM\_PWRBTN#
- 13(a) PM\_ME\_SLP\_LAN#
- 13(b) PM\_ME\_SLP\_A#
- 14 PM\_SUSC#
- 15 PM\_SUSB#
- 16 SUSC\_EC#  
+1.5V/+3V/+5V
- 17 SUSB\_EC#  
+0.6VS/+0.75VS/+1.5VS//+1.8VS/+3VS/+5VS  
+PEX\_VDD/+1.6VS6/+1.8VS6/+3VS6/+NVDD
- 20 SYSTEM\_PWRGD  
+VIT\_CPU
- 21 +VIT\_CPU\_PWRGD  
+0.8VS
- 22 +0.8VS\_PWRGD
- 23 ALL\_SYSTEM\_PWRGD
- 24 PM\_PCHPW0K
- 25 PM\_SYSPW0K
- 26 H\_DRAM\_PWRGD
- 27 H\_CPUPWRGD
- 28 SVID  
+VCORE
- +VccGFX
- 29 VRM\_PWRGD
- 30 SUS\_SATA#
- 31 BUF\_PLT\_RST#

## N56JZ Power Sequence (AC mode)

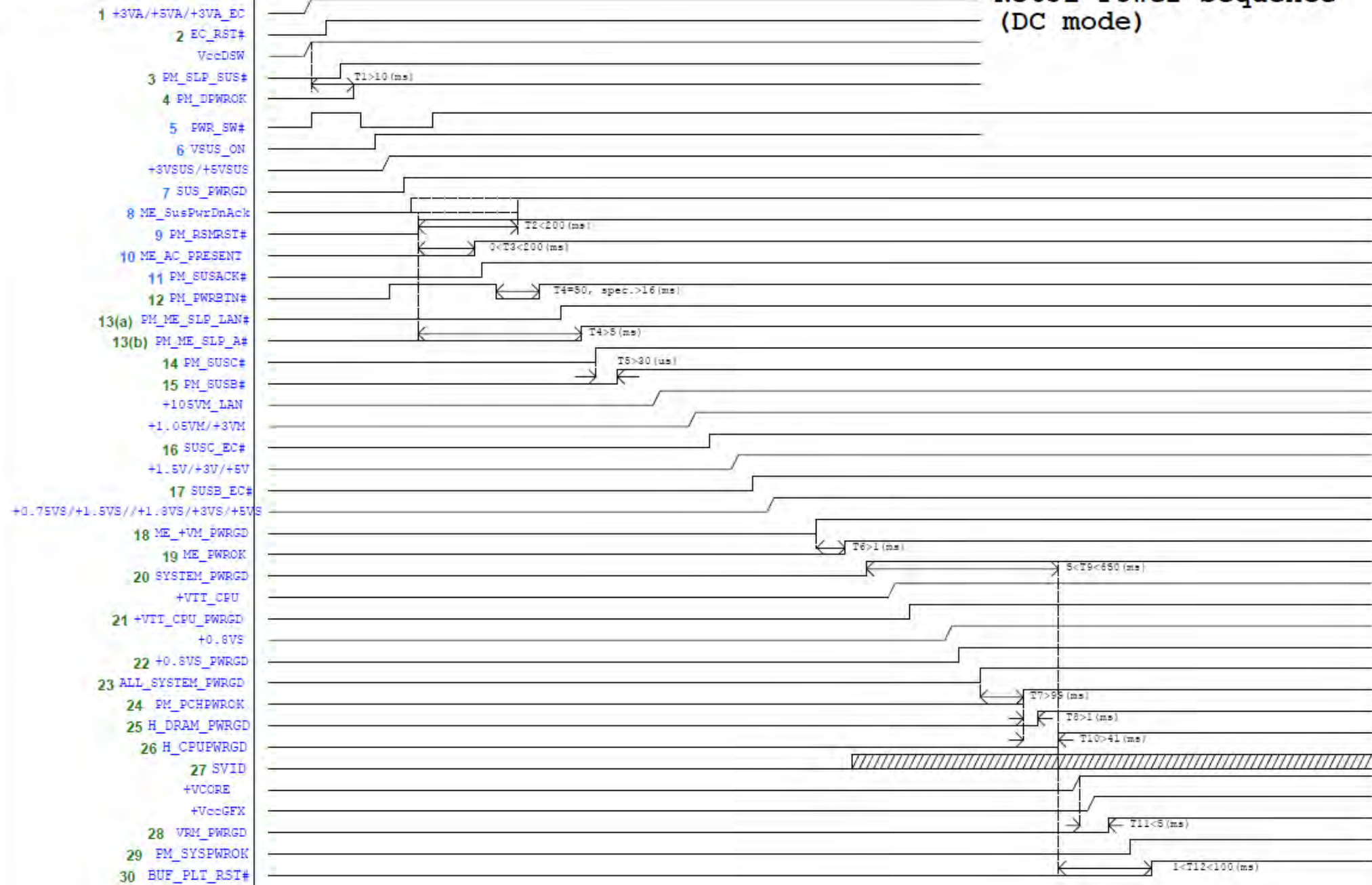




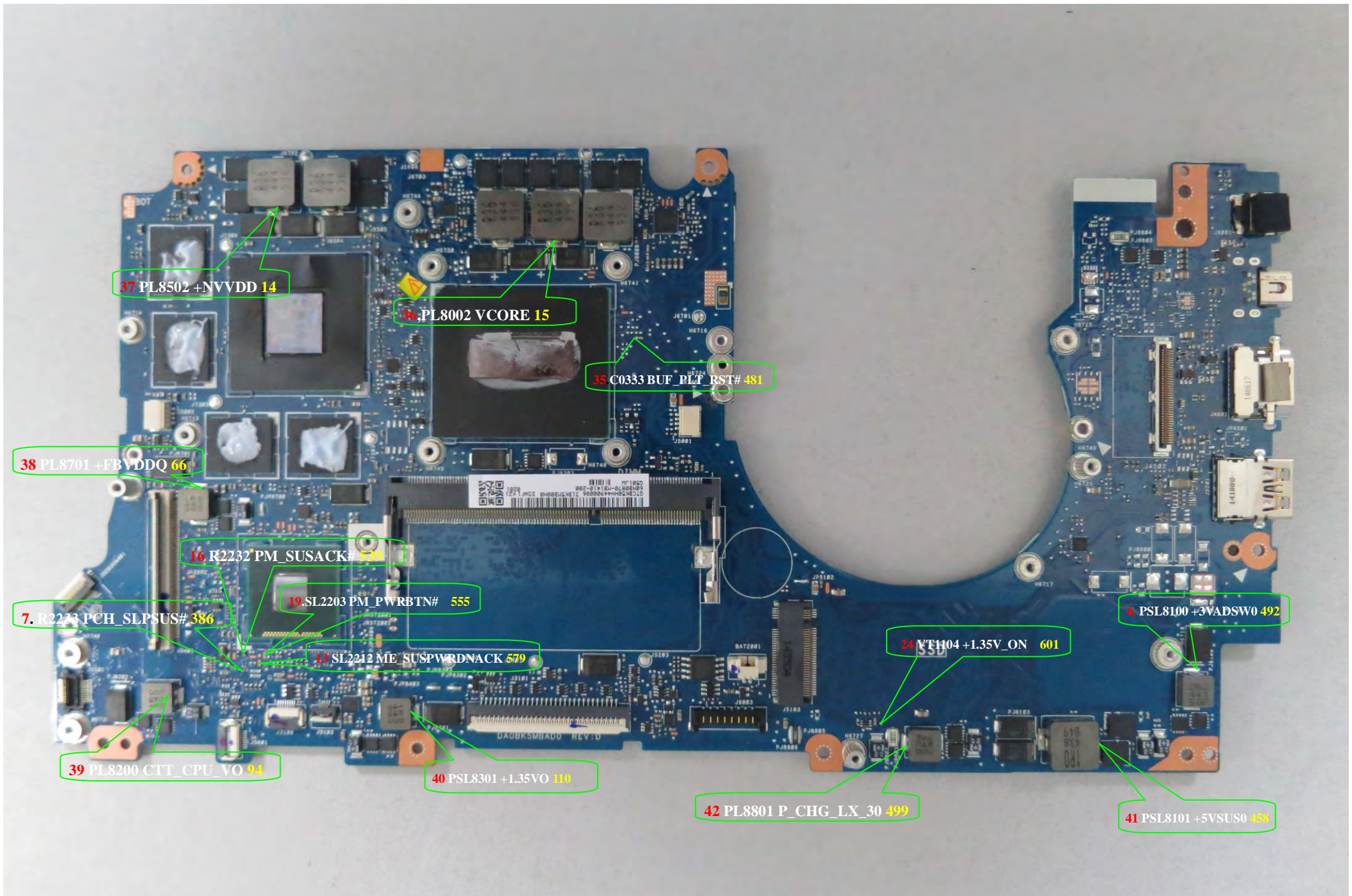
# DC\_IN POWER ON SEQUENCE

DC-IN Mode

N56JZ Power Sequence  
(DC mode)



## Signal Measure Point-Bottom





# Signal Measure Point-Top

